DEPARTMENT OF THE AIR FORCE
AIR FORCE RESEARCH LABORATORY
WRIGHT-PATTERSON AIR FORCE BASE OHIO

**Microelectronics Innovation for Next-Generation System**

**Advancement and Validation (MINSAV) Call 04**

Questions(Q) and Answers(A)

16 June 2025

Q - Would a chiplet based solution be considered responsive?

A - VEYRON is seeking monolithic ASIC designs, so a chiplet approach will be considered non-responsive.

Q - Are performance metrics considered requirements or goals?

A - Performance characteristics can be considered goals. Reductions in power consumption or improvements in RF characteristics are desirable, however this would be tied to the proposed transition path and measured against the technical and schedule risk associated with successfully completing the proposed design.