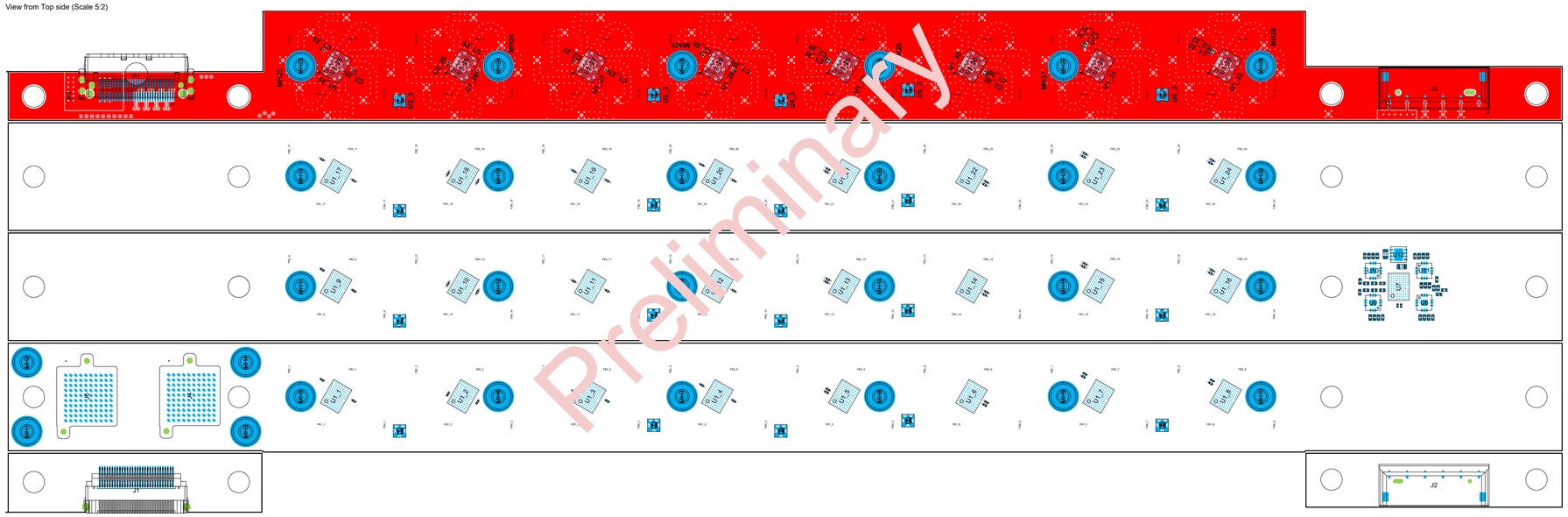
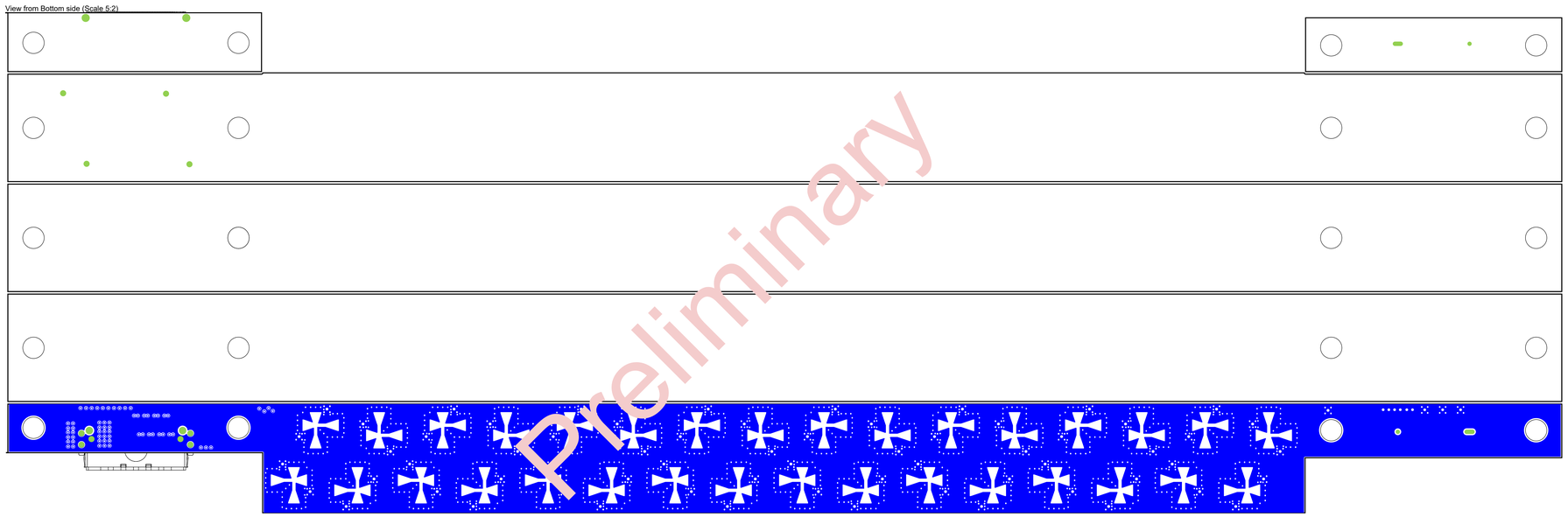
1. CERTIFICATE OF CONFORMANCE REQUIRED FOR ALL COMMERCIAL PARTS.
2. ESD HANDLING AND CONTROL PER ESTABLISHED PROCEDURES.
3. HANDLE ALL PARTS AND ASSEMBLIES WITH CLEAN, LINT-FREE, ANTI-STATIC GLOVES OR FINGER COTS
4. DEMOISTURIZE (BAKE OUT) PWB FOR A MINIMUM OF 2 HOURS AT 93°C ±5.5°C (200°F ±10°F.) PERFORM SOLDERING IMMEDIATELY AFTER BAKING, OR STORE IN A CLEAN, DRY, SEALED STATIC DISSIPATIVE BAG OR CONTAINER.
5. MARK THE ASSEMBLY WITH P/N AND S/N USING WHITE EPOXY INK OR A LABEL.
6. ASSEMBLY, SOLDERING, AND INSPECTION SHALL BE IN ACCORDANCE WITH J-STD-001 CLASS III, IPC-A-610 CLASS III, PROCESS PLAN, AND THE FOLLOWING NOTES:
   A. INSTALL AND SOLDER ALL SURFACE MOUNT (SMT) PART BEFORE THROUGH HOLE PARTS.
   B. FOR THROUGH HOLE PARTS, LEAD TERMINATIONS SHALL BE STRAIGHT THROUGH (NOT LAPPED OR CLINCHED.)
   C. PROCESS USING LEAD BASED, NO CLEAN SOLDER FORMULA
7. PLACE THE COMPLETED ASSEMBLY IN CLEAN, STATIC DISSIPATIVE PACKAGING. IDENTIFY PART BY TAGGING WITH PART NUMBER AND SERIAL/LOT NUMBER AS SPECIFIED IN WORK ORDER PER SAE AS478, METHOD 35. STORE IN A PROJECT APPROVED STORAGE AREA.
8. REFERENCE SCHEMATIC: ADaPT GEN III Test PCB.PDF
9. ALTIUM CONTROL FILES: ADaPT GEN III Test PCB.ZIP
10. PASSIVE (RESISTORS, CAPACITORS, INDUCTORS) COMPONENT PART NUMBERS SUBJECT TO CHANGE BASED ON MARKET AVAILABILITY.
    A. ANY ALTERNATE PARTS SELECTED WILL MEET OR EXCEED ORIGINAL SPECIFICATIONS
    B. ANY ALTERNATE PARTS SELECTED WILL CONFORM WITH ORIGINAL PARTS' FORM FACTOR
    C. SYSTEM DESIGN ENGINEER WILL APPROVE FINAL BILL OF MATERIALS.
11. PRIMARY SIDE (TOP LAYER) AND SECONDARY SIDE (BOTTOM LAYER) SHOWN ON PAGE ONE OF THIS DOCUMENT.
12. NO CHANGES BEYOND CUSTOMARY AND STANDARD ASSEMBLY PRACTICES SHALL BE MADE WITHOUT PRIOR APPROVAL FROM NASA GRC.

View from Top side (Scale 5:2)



View from Bottom side (Scale 5:2)



PART NO: =PCB_PART_NUMBER			
APPROVALS		DATE	NASA Glenn Research Center 21000 Brookpark Rd Cleveland, OH 44135
DESIGNER	=PCB_ENGINEER	=PCB_ENGINEER	
DESIGNER	=PCB_DESIGNER	=PCB_DESIGNER	
CHECKER	=PCB_CHECKER	=PCB_CHECKER	
Reference Documents			DWG/CAD FILE NAME: /Item — CURRENT REVISION/DATE: /ItemRevision
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FAB DOC:	=DOC_NO_FAB_DWG		SIZE: D — DWG NO: =DWG_NO — REV:
SCH DOC:	=DOC_NO_SCH_DWG		SCALE: =Scale — FILE NAME: ADaPT GEN III PCB, Assembly Drawing.PCBDwf — SHEET 1 OF 1
PCBA DOC:	=PCB_DWG_NO		