STATEMENT OF WORK

1. Objective/Requirements

The information listed below is required to complete the LCRD Sasken BCH Encoder/Decoder Renewal at Goddard Space Flight Center (GSFC).

2. Characteristics, Scope, and Specs

LINE	DESCRIPTION	QTY.
1	**BCH Encoder and Decoder IP (Sasken's IP)** Contractor's IP Deliverables are: 1. Verilog source code of the IP 2. Test benches developed in Verilog 3. IP integration document 4. Does not involve technical support **License Type:** **Single Project Source Code License** As a Single Project License, Valid for 3 Years, Licensee will have certain rights to a Single Use of contractor's IP for implementation in a FPGA based hardware system (Licensee System). **Licensee System** means a System owned by the Licensee and specifically identified in this Quotation and the Purchase Order and which contains one or more instantiation of contractor's IP. Single Use means one or more instantiation of contractor's IP only in the Licensee. System as specified in the Purchase Order. Any additional use of any aspect of the contractor's IP in a new or different Licensee System (i.e., one that functions differently from the Licensee System that was the subject of the initial use, such as the Licensee System that implements a change in functionality or timing, electrical or power specifications), will be considered as Reuse and will have to be purchased separately as a Re-use License. However, use of contractor's IP in a revised version of the same Licensee System, where the only change is to implement bug fixes or error corrections in the System will not be considered Reuse.	1

3. Place of Performance

NASA Goddard Labs,
Technology Enterprise and Mission Pathfinder Office (TEMPO) Code 450.2,
Exploration and Space Communications (ESC) Projects Division
[redacted]

4. Period of Performance

7/1/2026 – 6/30/2029